

Figure - Bug #111770

Bug # 111192 (CLOSED): 【Figure】【EVT3】【EE】【启动时间1分6秒】【Performance】

geni_i2c_driver_init模块初始化不接屏耗时6.6s，接屏后接近3s。包括MAX975X和cyttsp7的初始化。

2022-09-06 18:19 - CD SYSTEM-龚元祝

Status:	CLOSED	Start date:	2022-09-06
Priority:	Normal	Due date:	
Assignee:	移动测试一组_CDTS 刘强	% Done:	0%
Category:	BSP	Estimated time:	0.00 hour
Target version:	VX1_MCE_FSE_V3.0_20221030	Found Version:	every version
Need_Info:	--	Degraded:	--
Resolution:	--	Verified Version:	
Severity:	Normal	Fixed Version:	
Reproducibility:	Every time	Root cause:	
Test Type:	Bring Up Test		
Description			
1 MAX975X的两个msleep 500ms需要优化。			
2 cyttsp7的初始化耗时需要优化，尤其是接屏后还出现I2C error导致delay500ms，需要分析解决。			

History

#1 - 2022-09-14 11:17 - CD TPM-申艳艳

- Category set to BSP

#2 - 2022-09-21 12:18 - CD BSP-余霄

- Status changed from New to RESOLVED

- Assignee changed from CD BSP-余霄 to CD SYSTEM-赵正军

如之前同步，已修改相关耗时操作，请确认。

Patch:

<https://dev.thundercomm.com/gerriit/c/general/kernel/msm-4.19/+ /145461>

#3 - 2022-09-21 14:44 - CD SYSTEM-龚元祝

- Status changed from RESOLVED to ASSIGNED

- Assignee changed from CD SYSTEM-赵正军 to CD BSP-余霄

cyttsp7耗时的问题还没解决。

```
Line 943: [ 2.352760] cyttsp7_i2c_adapter 0-0024: cyttsp7_init: INIT CYTTSP RST gpio=1106 and IRQ gpio=1107 ERR gpio=1123
Line 943: [ 2.352760] cyttsp7_i2c_adapter 0-0024: cyttsp7_init: INIT CYTTSP RST gpio=1106 and IRQ gpio=1107 ERR gpio=1123
Line 944: [ 2.444035] cyttsp7_i2c_adapter 0-0024: cyttsp7_xres: RESET CYTTSP gpio=1106 r=0
Line 944: [ 2.444035] cyttsp7_i2c_adapter 0-0024: cyttsp7_xres: RESET CYTTSP gpio=1106 r=0 * 卡430ms*
Line 945: [ 2.875842] cyttsp7_i2c_adapter 0-0024: cyttsp7_si_get_test_data_: SCANNING is ENABLED codel=1E
Line 945: [ 2.875842] cyttsp7_i2c_adapter 0-0024: cyttsp7_si_get_test_data_: SCANNING is ENABLED codel=1E
Line 946: [ 2.883055] cyttsp7_i2c_adapter 0-0024: TTSP Protocol Version is 3.2
Line 947: [ 2.926405] cyttsp7_i2c_adapter 0-0024: cyttsp7_startup_: cyttsp7_exit startup rc=0...
Line 947: [ 2.926405] cyttsp7_i2c_adapter 0-0024: cyttsp7_startup_: cyttsp7_exit startup rc=0...
Line 947: [ 2.926405] cyttsp7_i2c_adapter 0-0024: cyttsp7_startup_: cyttsp7_exit startup rc=0...
Line 948: [ 2.948243] input: cyttsp7_mt as /devices/platform/soc/980000.i2c/i2c-0/0-0024/input/input2
```

Line 949: [2.948266] cyttsp7_i2c_adapter 0-0024: cyttsp7_wakeup: WAKEUP CYTTSP gpio=1107 r=0
Line 949: [2.948266] cyttsp7_i2c_adapter 0-0024: cyttsp7_wakeup: WAKEUP CYTTSP gpio=1107 r=0 * 卡500ms*
Line 952: [3.456209] cyttsp7_i2c_adapter 0-0024: cyttsp7_awake_device_from_deep_sleep: Queueing startup
Line 952: [3.456209] cyttsp7_i2c_adapter 0-0024: _cyttsp7_awake_device_from_deep_sleep: Queueing startup
Line 953: [3.456221] cyttsp7_i2c_adapter 0-0024: _cyttsp7_queue_startup: cyttsp7_startup queued
Line 953: [3.456221] cyttsp7_i2c_adapter 0-0024: _cyttsp7_queue_startup: cyttsp7_startup queued
Line 953: [3.456221] cyttsp7_i2c_adapter 0-0024: _cyttsp7_queue_startup: cyttsp7_startup queued
Line 954: [3.548046] cyttsp7_i2c_adapter 0-0024: cyttsp7_xres: RESET CYTTSP gpio=1106 r=0
Line 954: [3.548046] cyttsp7_i2c_adapter 0-0024: cyttsp7_xres: RESET CYTTSP gpio=1106 r=0
卡430ms
Line 955: [3.980065] cyttsp7_i2c_adapter 0-0024: cyttsp7_si_get_test_data: SCANNING is ENABLED code=1E
Line 955: [3.980065] cyttsp7_i2c_adapter 0-0024: cyttsp7_si_get_test_data: SCANNING is ENABLED code=1E
Line 956: [3.987245] cyttsp7_i2c_adapter 0-0024: TTSP Protocol Version is 3.2
Line 957: [4.030648] cyttsp7_i2c_adapter 0-0024: cyttsp7_startup_: cyttsp7_exit startup rc=0...
Line 957: [4.030648] cyttsp7_i2c_adapter 0-0024: cyttsp7_startup_: cyttsp7_exit startup rc=0...
Line 957: [4.030648] cyttsp7_i2c_adapter 0-0024: cyttsp7_startup_: cyttsp7_exit startup rc=0...
TP的初始化有问题，还卡了1.3s。请解决。

#4 - 2022-09-21 14:45 - CD SYSTEM-龚元祝

- File d0918.txt added

#5 - 2022-09-27 11:13 - 物联网项目组-RD3_CDTs 周飞

- Target version set to VX1_MCE_FSE_V3.0_20221030

#6 - 2022-10-19 10:17 - CD BSP-余霄

- Assignee changed from CD BSP-余霄 to CD SYSTEM-龚元祝

已修复该问题

代码:

<https://dev.thundercomm.com/gerrit/c/general/kernel/msm-4.19/+/154363>

版本 :

ftp://cdiot@192.168.87.46/Pre_figure/VerifyBuild/Pre_figure_turbox-c2130c-la1.1-qssi12-dev/20221018/202210181821-1785/FlatBuild_VX1_MCE_xx.xx_FSE.V.userdebug.0.0.0.20221018.alpha.zip

#7 - 2022-10-19 14:19 - CD SYSTEM-龚元祝

- Status changed from ASSIGNED to RESOLVED

- Assignee changed from CD SYSTEM-龚元祝 to CDTs_TEST 王成

请测试的同事验证确认。谢谢！

#8 - 2022-10-19 14:21 - CDTs_TEST 王成

- Assignee changed from CDTs_TEST 王成 to SZTS_TEST 邹涛

#9 - 2022-10-19 20:48 - SZTS_TEST 邹涛

- File Snipaste_2022-10-19_20-47-26.png added

- File Serial-COM78_20221019203929.log added

版本：

ftp://cdiot@192.168.87.46/Pre_figure/VerifyBuild/Pre_figure_turbox-c2130c-la1.1-qssi12-dev/20221018/202210181821-1785/FlatBuild_VX1_MCE_xx.xx_FSE.V.userdebug.0.0.0.20221018.alpha.zip

evt2板子烧录上面版本后，抓取串口日志见附件

#10 - 2022-10-19 20:56 - SZTS_TEST 邹涛

- File Snipaste_2022-10-19_20-47-26.png added
- Status changed from RESOLVED to VERIFY_FAILED
- Assignee changed from SZTS_TEST 邹涛 to CD SYSTEM-龚元祝

版本：

ftp://cdiot@192.168.87.46/Pre_figure/VerifyBuild/Pre_figure_turbox-c2130c-la1.1-qssi12-dev/20221018/202210181821-1785/FlatBuild_VX1_MCE_xx.xx_FSE.V.userdebug.0.0.0.20221018.alpha.zip

evt2板子烧录上面版本后，抓取串口日志查看，问题未修复

#11 - 2022-10-19 20:56 - SZTS_TEST 邹涛

- Assignee changed from CD SYSTEM-龚元祝 to CD BSP-余霄

#12 - 2022-10-21 16:06 - CD BSP-余霄

- Status changed from VERIFY_FAILED to ASSIGNED

分析 两处导致延时的原因均为初始化touch时

等待bootloader切换为sysinfo模式时导致。 wait_event_timeout

kernel/msm-4.19/drivers/input/touchscreen/cyttsp7/cyttsp7_core.c

LINE : 3529 cyttsp7_wait_sysinfo_mode

1.尝试将wait动作去掉，初始化失败

2.尝试使用ldr_fast_exit，任无改善

```
[ 16.947254] cyttsp7_i2c_adapter 0-0024: cyttsp7_init: INIT CYTTSP RST gpio=1106 and IRQ gpio=1107 ERR gpio=1123
[ 17.143722] cyttsp7_i2c_adapter 0-0024: exit_bootloader: Issuing ldr_fast_exit
[ 17.580487] cyttsp7_i2c_adapter 0-0024: cyttsp7_si_get_test_data: SCANNING is ENABLED codel=1E
[ 17.598117] cyttsp7_i2c_adapter 0-0024: TTSP Protocol Version is 3.2
[ 17.657649] cyttsp7_i2c_adapter 0-0024: cyttsp7_startup_: cyttsp7_exit startup rc=0...
[ 17.691183] input: cyttsp7_mt as /devices/platform/soc/980000.i2c/i2c-0/0-0024/input/input2
[ 17.700006] cyttsp7_i2c_adapter 0-0024: cyttsp7_wakeup: WAKEUP CYTTSP gpio=1107 r=0
[ 17.708988] i2c_geni 980000.i2c: i2c error :-107
[ 18.240824] i2c_geni 980000.i2c: i2c error :-107
[ 18.245705] cyttsp7_i2c_adapter 0-0024: cyttsp7_awake_device_from_deep_sleep: Queueing startup
[ 18.255092] cyttsp7_i2c_adapter 0-0024: _cyttsp7_queue_startup: cyttsp7_startup queued
[ 18.352254] cyttsp7_i2c_adapter 0-0024: cyttsp7_xres: RESET CYTTSP gpio=1106 r=0
[ 18.361560] cyttsp7_i2c_adapter 0-0024: _exit_bootloader: Issuing ldr_fast_exit
[ 18.798692] cyttsp7_i2c_adapter 0-0024: cyttsp7_si_get_test_data: SCANNING is ENABLED codel=1E
[ 18.816423] cyttsp7_i2c_adapter 0-0024: TTSP Protocol Version is 3.2
[ 18.875975] cyttsp7_i2c_adapter 0-0024: cyttsp7_startup_: cyttsp7_exit startup rc=0...
```

该问题软件测无法修复，请确认。。

#13 - 2022-10-21 17:51 - CD BSP-余霄
- Status changed from ASSIGNED to RESOLVED
- Assignee changed from CD BSP-余霄 to CD SYSTEM-龚元祝

#14 - 2022-10-21 17:52 - CD BSP-余霄

不接屏和接屏时间不一致问题已修复，请确认

#15 - 2022-10-25 10:16 - CD SYSTEM-龚元祝
- Assignee changed from CD SYSTEM-龚元祝 to CDTS_TEST 王成

结果：
1 MAX975X初始化时间已优化。
2 cyttsp7初始化时间无法优化。
3 不接屏和接屏的开机时间不一致的问题已修复。

请测试用以下版本验证第3点：
http://10.0.76.24:8080/job/VerifyBuild_for_IOT_6490/1918/
验证点：不接屏和接屏的开机时间应该一致

#16 - 2022-10-28 10:17 - CDTS_TEST 王成
- Assignee changed from CDTS_TEST 王成 to 移动测试一组_CDTS 刘强

#17 - 2022-10-28 16:27 - 移动测试一组_CDTS 刘强
- Status changed from RESOLVED to VERIFIED

验证通过：
版本号：01.00.X101_20221028_private_user

1 MAX975X初始化时间已优化。
2 cyttsp7初始化时间无法优化。
3 不接屏和接屏的开机时间不一致的问题已修复。

|| 不接屏和接屏的开机时间基本一致

接屏：43秒36ms 到主界面
不接屏：43秒79ms 到主界面

#18 - 2022-10-28 16:46 - 移动测试一组_CDTS 刘强
- Status changed from VERIFIED to CLOSED

Files			
d0918.txt	257 KB	2022-09-21	CD SYSTEM-龚元祝
Snipaste_2022-10-19_20-47-26.png	31.5 KB	2022-10-19	SZTS_TEST 邹涛

Serial-COM78_20221019203929.log	217 KB	2022-10-19	SZTS_TEST 邹涛
Snipaste_2022-10-19_20-47-26.png	31.5 KB	2022-10-19	SZTS_TEST 邹涛